

Form PTO-1449 (Modified)  <b>LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT</b> (Use several sheets if necessary)	Atty Docket No.: TI-33257	Serial No.: <del>Unknown</del>
	Applicant: Richard Kane Stair, <i>et al.</i>	
	Filing Date: Herewith	Group: <del>Unknown</del> 2816

10/15/01  
 11046 U.S. PTO  
 09/977609

**REFERENCE DESIGNATION U.S. PATENT DOCUMENTS**

EXAMINER INITIAL		DOCUMENT NUMBER							DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
<i>M</i>	AA	5	9	5	2	8	7	4	09/14/1999	Manaresi, et al.	327	541	12/19/1995
<i>M</i>	AB	6	0	8	4	4	7	5	07/04/2000	Rincon-Mora	330	255	10/06/1998

**FOREIGN PATENT DOCUMENTS**

Examiner Initial		Document Number							Date	Country	Class	Subclass	Translation	
													Yes	No
	AC													

**OTHER ART(Including Author, Title, Date, Pertinent Pages, etc.)**

<i>M</i>	AD	X. Zhou, <i>et al.</i> ; "Threshold Voltage Definition and Extraction for Deep-Submicron MOSFET's", <i>Solid-State Electronics</i> , Revised October 17, 2000, p. 1-3
<i>M</i>	AE	A. Ortiz-Conde, <i>et al.</i> ; "A New Approach to Extract the Threshold Voltage of MOSFET's", <i>IEEE Transactions on Electron Devices</i> , vol. 44, no. 9, September 1997, p. 1523-1528
<i>M</i>	AF	G. Alfonso Rincon-Mora; "Active Capacitor Multiplier in Miller-Compensated Circuits", <i>IEEE</i> , 1999, p. 1-15
<i>M</i>	AG	C. Galup-Montoro, <i>et al.</i> ; "MOSFET Threshold Extraction from Voltage-Only Measurements", <i>Electronics Letters</i> , vol. 30, no. 17, August 18, 1994, p. 1458-1459
<i>M</i>	AH	N. Manaresi, <i>et al.</i> ; "MOSFET Threshold Extraction Circuit", <i>Electronics Letters</i> , vol. 31, no. 17, August 17, 1995, p. 1434-1435
<i>M</i>	AI	J. Ramirez-Angulo, <i>et al.</i> ; "Low-Voltage CMOS Op-Amp with Rail-to-Rail Input and Output Signal Swing for Continuous-Time Signal Processing Using Multiple-Input Floating-Gate Transistors", <i>IEEE Transactions on Circuits and Systems</i> , vol. 48, no. 1, January 2001, p. 111-116
<i>M</i>	AJ	J. Ramirez-Angulo, <i>et al.</i> ; "MITE Circuits: The Continuous-Time Counterpart to Switched-Capacitor Circuits", <i>IEEE Transactions on Circuits and Systems</i> , vol. 48, no. 1, January 2001, p. 45-55
<i>M</i>	AK	L. Dobrescu, <i>et al.</i> ; "Threshold Voltage Extraction Methods for MOS Transistors", <i>IEEE</i> , 2000, p. 371-374
<i>M</i>	AL	"Slew Rate Control of LVDS Circuits", <i>Application Report</i> , Texas Instruments Incorporated, March 1999, p. 1-12

EXAMINER <i>howe</i>	DATE CONSIDERED 7/10/02
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EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.